

### 56.8mm (2.3 INCH) 16 SEGMENT SINGLE DIGIT ALPHANUMERIC DISPLAY

Part Number: PSC23-11SYKWA

Super Bright Yellow

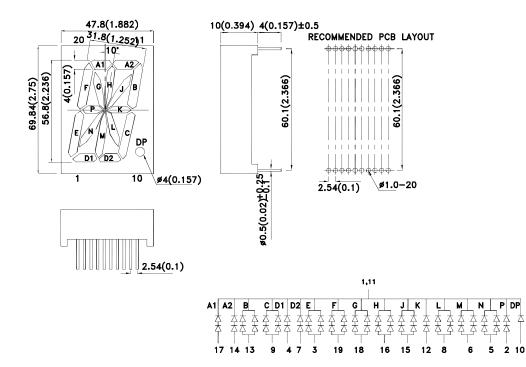
#### **Features**

- 2.3 inch character height.
- Low current operation.
- High contrast and light output.
- Easy mounting on P.C. boards or sockets.
- Mechanically rugged.
- Standard : gray face, white segment.
- RoHS compliant.

### **Description**

The Super Bright Yellow device is made with AlGaInP (on GaAs substrate) light emitting diode chip.

### **Package Dimensions& Internal Circuit Diagram**



#### Notes



2. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

 SPEC NO: DSAK2866
 REV NO: V.5A
 DATE: MAY/10/2013
 PAGE: 1 OF 8

 APPROVED: WYNEC
 CHECKED: Joe Lee
 DRAWN: F.Cui
 ERP: 1311000361



### **Selection Guide**

Part No.	Dice	Lens Type	lv (ucd) [1] @ 10mA		Description
			Min.	Тур.	
PSC23-11SYKWA	Super Bright Yellow (AlGaInP)	White Diffused	52000	170000	Common Cathode, Rt. Hand Decimal.
			*21000	*49000	

- Luminous intensity/ luminous Flux: +/-15%.
   \* Luminous intensity value is traceable to the CIE127-2007 compliant national standards.

Electrical / Optical Characteristics at TA=25°C

Symbol		Parameter	Device	Тур.	Max.	Units	Test Conditions
λ peak	Peak Wavelength		Super Bright Yellow	590		nm	IF=20mA
λ D[1]	Dominant Wavelength		Super Bright Yellow	590		nm	IF=20mA
Δλ 1/2	Spectral Line Half-width		Super Bright Yellow	20		nm	IF=20mA
С	Capacitance		Super Bright Yellow	20		pF	VF=0V;f=1MHz
		A1,A2,D1,D2,P,K		4.0	5.0		
V <sub>F</sub> [2] Forward Voltage	B,C,E,F,G,H,J,L,M,N	Super Bright Yellow	4.0	5.0	V	IF=20mA	
	remage	DP		2.0	2.5		
Reverse In Current	A1,A2,D1,D2,P,K			10			
	B,C,E,F,G,H,J,L,M,N	Super Bright Yellow		20	uA	VR = 5V	
	(Per Chip)	DP			10		

### Notes:

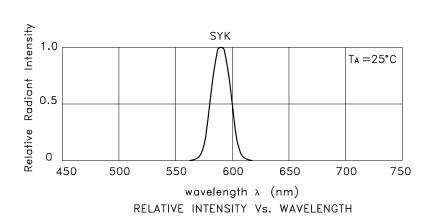
- 1.Wavelength: +/-1nm. 2. Forward Voltage: +/-0.1V.
- 3. Wavelength value is traceable to the CIE127-2007 compliant national standards.

Absolute Maximum Ratings at TA=25°C

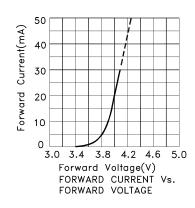
Paran	neter	Super Bright Yellow	Units	
	A1,A2,D1,D2,P,K	150	mW	
Power dissipation	B,C,E,F,G,H,J,L,M,N	300		
	DP	75		
	A1,A2,D1,D2,P,K	30	mA	
DC Forward Current	B,C,E,F,G,H,J,L,M,N	60		
	DP	30		
	A1,A2,D1,D2,P,K	175		
Peak Forward Current [1]	B,C,E,F,G,H,J,L,M,N	350	mA	
	DP	175		
	A1,A2,D1,D2,P,K	5		
Reverse Voltage (Per Chip)	B,C,E,F,G,H,J,L,M,N	5	V	
	DP	5		
Operating / Storage Temperature		-40°C To +85°C		
Lead Solder Temperature [2]		260°C For 3 Seconds		

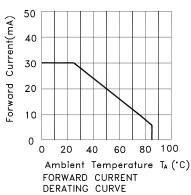
- 1. 1/10 Duty Cycle, 0.1ms Pulse Width.
- 2. 2mm below package base.

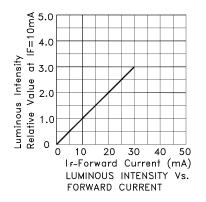
SPEC NO: DSAK2866 **REV NO: V.5A** DATE: MAY/10/2013 PAGE: 2 OF 8 APPROVED: WYNEC **CHECKED:** Joe Lee DRAWN: F.Cui ERP: 1311000361

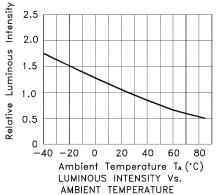


### Super Bright Yellow PSC23-11SYKWA



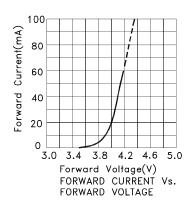


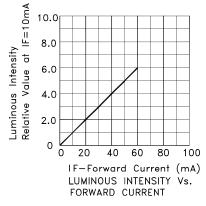


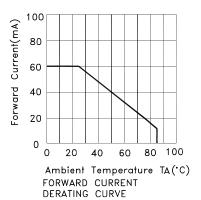


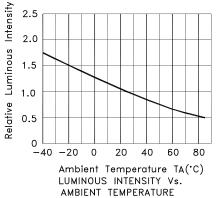
Note: the curves are on the segment a1,a2,d1,d2,p,k.

SPEC NO: DSAK2866 APPROVED: WYNEC REV NO: V.5A CHECKED: Joe Lee DATE: MAY/10/2013 DRAWN: F.Cui PAGE: 3 OF 8 ERP: 1311000361



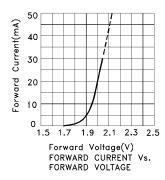


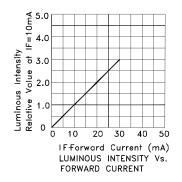


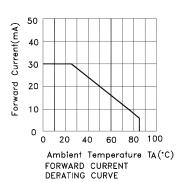


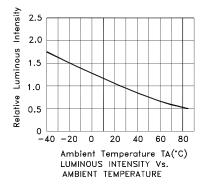
Note: the curves are on the segment b,c,e,f,g,h,j,l,m,n.

SPEC NO: DSAK2866 REV NO: V.5A DATE: MAY/10/2013 PAGE: 4 OF 8
APPROVED: WYNEC CHECKED: Joe Lee DRAWN: F.Cui ERP: 1311000361





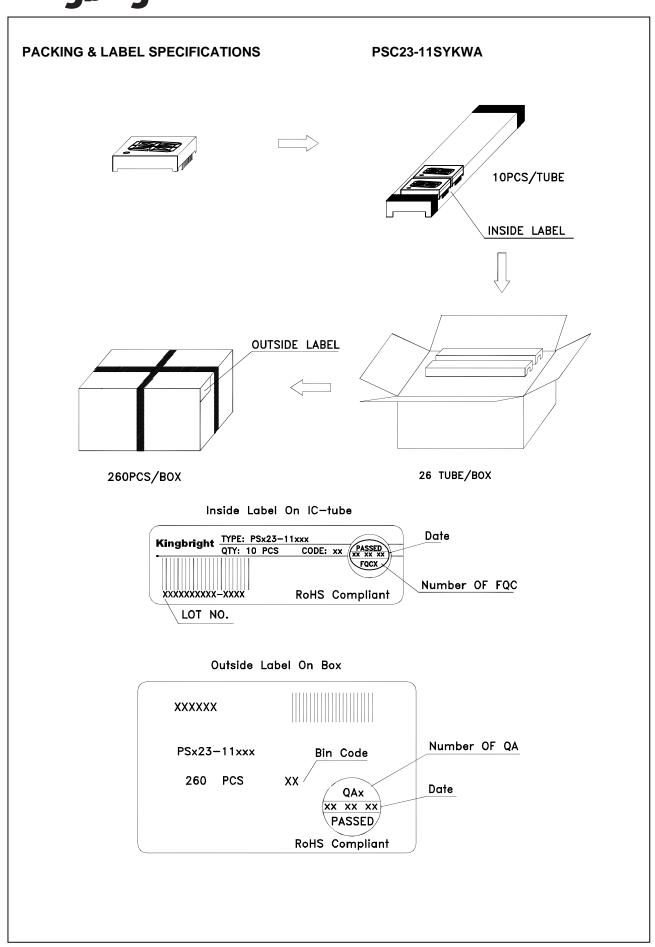




Note:the curves are on the DP.

SPEC NO: DSAK2866 REV NO: V.5A DATE: MAY/10/2013 PAGE: 5 OF 8

APPROVED: WYNEC CHECKED: Joe Lee DRAWN: F.Cui ERP: 1311000361

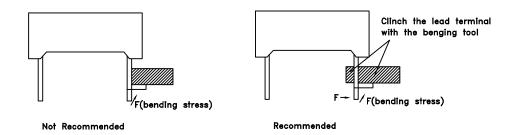


SPEC NO: DSAK2866 APPROVED: WYNEC REV NO: V.5A CHECKED: Joe Lee DATE: MAY/10/2013 DRAWN: F.Cui PAGE: 6 OF 8 ERP: 1311000361

### THROUGH HOLE DISPLAY MOUNTING METHOD

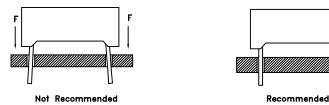
### Lead Forming

Do not bend the component leads by hand without proper tools. The leads should be bent by clinching the upper part of the lead firmly such that the bending force is not exerted on the plastic body.

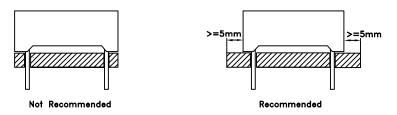


### Installation

- 1. The installation process should not apply stress to the lead terminals.
- 2. When inserting for assembly, ensure the terminal pitch matches the substrate board's hole pitch to prevent spreading or pinching the lead terminals.



3. The component shall be placed at least 5mm from edge of PCB to avoid damage caused excessive heat during wave soldering.

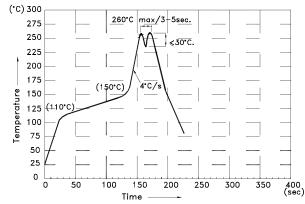


SPEC NO: DSAK2866 REV NO: V.5A DATE: MAY/10/2013 PAGE: 7 OF 8

APPROVED: WYNEC CHECKED: Joe Lee DRAWN: F.Cui ERP: 1311000361

### DISPLAY SOLDERING CONDITIONS

Wave Soldering Profile For Lead-free Through-hole LED.



#### NOTES:

- 1.Recommend the wave temperature 245°C~260°C.The maximum soldering temperature should be less than 260°C.
- 2.Do not apply stress on epoxy resins when temperature is over 85°C.
- 3. The soldering profile apply to the lead free soldering (Sn/Cu/Ag alloy).
- $extstyle{4.}$  During wave soldering , the PCB top-surface temperature should be kept below 105°C

#### 5.No more than once.

### Soldering General Notes:

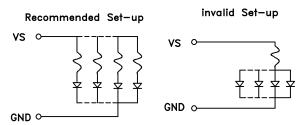
- 1. Through—hole displays are incompatible with reflow soldering.
- 2. If components will undergo multiple soldering processes, or other processes where the components may be subjected to intense heat, please check with Kingbright for compatibility.

### **CLEANING**

- 1.Mild "no-clean" fluxes are recommended for use in soldering.
- 2. If cleaning is required, Kingbright recommends to wash components with water only. Do not use harsh organic solvents for cleaning, because they may damage the plastic parts .And the devices should not be washed for more than one minute.

### CIRCUIT DESIGN NOTES

- 1.Protective current—limiting resistors may be necessary to operate the Displays.
- 2.LEDs mounted in parallel should each be placed in series with its own current—limiting resistor.



Detailed application notes are listed on our website. <a href="http://www.kingbright.com/application\_notes">http://www.kingbright.com/application\_notes</a>

SPEC NO: DSAK2866 APPROVED: WYNEC REV NO: V.5A CHECKED: Joe Lee DATE: MAY/10/2013 DRAWN: F.Cui PAGE: 8 OF 8 ERP: 1311000361